OneSpin Announces Immediate Availability of OneSpin 360 EC-FPGA Tool Qualification Kit Certified for ISO 26262, IEC 61508, EN 50128

Users Deploy EC-FPGA Formal Sequential Equivalence Checking into Automotive ASIL D Projects with no Additional Tool Qualification Requirements

Munich, – February 14, 2018 – OneSpin® Solutions, provider of innovative formal verification solutions for highly reliable, digital integrated circuits (ICs), announced immediate availability of its OneSpin 360 ECFPGA ™ Tool Qualification Kit certified by internationally-recognized testing body TÜV SÜD. The certification states that EC-FPGA meets the most stringent tool qualification criteria set by functional safety standards ISO 26262 (TCL3/ASIL D), IEC 61508 (T2/SIL 3) and EN 50128 (T2/SIL 3).

"OneSpin's EC-FPGA is the only EDA tool to exhaustively verify highly optimized FPGAs for autonomous vehicles, aerospace, nuclear power plants, railways and medical devices to eliminate systematic errors introduced during implementation," notes Dr. Raik Brinkmann, OneSpin's president and CEO. "With this certified Tool Qualification Kit, our users can deploy EC-FPGA seamlessly in their safety-critical flows to achieve a new level of productivity and standard compliance."

EC-FPGA is an automatic sequential equivalence checker that prevents fieldprogrammable gate array (FPGA) design flows from introducing synthesis, place-androute and other implementation errors. Safety standards require rigorous verification before production to minimize the risk of failures in the field. Using advanced formal technology, EC-FPGA detects corner-case design flow bugs with a process that is orders of magnitude more efficient and rigorous than gate-level simulation.

OneSpin also announced today that its general tool development and testing process has been audited by TÜV SÜD. (For more details, see accompanying news release dated February 14: "OneSpin Completes Series of Factory Inspections, Audits by Internationally-Recognized Testing Body TÜV SÜD.") Additional Tool Qualification Kits for other OneSpin tools will be available later in the year.

Pricing and Availability

Pricing and availability info via email: sales@evision-systems.de

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About eVision Systems GmbH

eVision Systems GmbH supports companies targeting the development of microelectronics, with a substantial portfolio of measuring- and testing equipment, electronic design automation (EDA) development tools and services.

Since we founded eVision Systems GmbH, it has always been our goal to help young, innovative companies enter the Central European market. It is common to all their products that the technology is unique, that they are more than an alternative to established solutions and that they enhance and complement existing design flows. Safety in the design, reusability and increased productivity as a result, are the success factors of our customers.

Acceleration of the simulation and verification of complex algorithm through C to FPGA synthesis, HDL simulation and verification with Linting and Code Coverage for ASIC and FPGA, board and chip packaging design are a few of the topics we are addressing with these emergent and trend setting products.

Together with ALDEC, CDS, Impulse Accelerated, One Spin Solutions, Pico Computing, Sigasi and Total Phase, we work with customers throughout Central Europe.

eVision Systems is the authorized total phase distributor for Germany, Switzerland and Austria. Our goal is to offer the best price to our customers. In addition to the favorable prices we offer local, german-speaking service and we usually deliver within one day from the warehouse in Munich.

You can find more information on our website: www.evision-systems.de or on our onlineshop www.evision-webshop.de.

About Onespin

OneSpin Solutions has emerged as a leader in formal verification through a range of advanced electronic design automation (EDA) solutions for digital integrated circuits. Headquartered in Munich, Germany, OneSpin is passionate about enabling users to address design challenges in areas where reliability really counts: safety-critical verification, SystemC/C++ high-level synthesis (HLS) code analysis and FPGA equivalence checking. OneSpin's advanced formal verification platform and dedication to getting it right the first time have fueled dramatic growth over the past five years as the company forges partnerships with leading electronics suppliers to pursue design perfection. OneSpin: Making Electronics Reliable.