

Aldec Delivers Verification Support for Embedded Applications with New Xilinx Zynq-based TySOM Embedded Development Kit

- *The TySOM™ Embedded Development Kit is for the embedded designer who needs a high-performance RTL simulator with advanced RTL Debugging features for their embedded applications such as Industrial IoT, Factory Automation, UAV, Automotive, Robotics and Computer Vision*

Forstinning 07. Dezember 2016 –eVision Systems is exhibiting at the Embedded World 2017 the TySOM™ Embedded Development Kit. The Kit has following features and advantages.

- **Zynq Development Board** containing programmable FPGA and dual ARM® Cortex-A9 cores for embedded designers developing complex systems with custom RTL and embedded applications. The board includes memories (DDR3, uSD), communication interfaces (miniPCIe, Ethernet, USB, Pmod, JTAG) multimedia interfaces (HDMI, audio, CMOS camera) and daughter board expansions via FMC.
- **Supports Xilinx® Vivado™ and SDK™ development flow.** Zynq-based hardware designs are created in Vivado and simulated in Riviera-PRO as the target simulator in Vivado. Software applications created in SDK are executed on the TySOM board as bare-metal or Linuxbased applications. Riviera-PRO and Vivado cooperation is powered by the Xilinx TCL Store Aldec Integration App.
- **Pre-Validated Ubuntu Embedded Host Reference Design** where users are able to turn the TySOM board into a full-stacked Linux box, compile and run complex Linux applications and learn the basics of the main Linux concepts and frameworks. The reference design contains the hardware (Vivado design) and software (Linux setup) support for the next groups of peripherals used with the TySOM board.

eVision Systems comprehensive portfolio of measuring and test equipment, FPGA development tools, and services will also be shown at the exhibition.

For more information visit the eVision Systems Booth 4-560, 14.-16. March 2017 at the embedded World show in Nuremberg.

Pricing and Availability

Pricing and availability info via email: sales@evision-systems.de

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About eVision Systems GmbH

eVision Systems GmbH supports companies targeting the development of microelectronics, with a substantial portfolio of measuring- and testing equipment, electronic design automation (EDA) development tools and services.

Since we founded eVision Systems GmbH, it has always been our goal to help young, innovative companies enter the Central European market. It is common to all their products that the technology is unique, that they are more than an alternative to established solutions and that they enhance and complement existing design flows. Safety in the design, reusability and increased productivity as a result, are the success factors of our customers.

Acceleration of the simulation and verification of complex algorithm through C to FPGA synthesis, HDL simulation and verification with Linting and Code Coverage for ASIC and FPGA , board and chip packaging design are a few of the topics we are addressing with these emergent and trend setting products.

Together with ALDEC, CDS, Impulse Accelerated, Introspect Technology, One Spin Solutions, Pico Computing, Sigasi and Total Phase, we work with customers throughout Central Europe.

eVision Systems is the authorized total phase distributor for Germany, Switzerland and Austria. Our goal is to offer the best price to our customers. In addition to the favorable prices we offer local, german-speaking service and we usually deliver within one day from the warehouse in Munich.

You can find more information on our website: www.evision-systems.de or on our onlineshop www.evision-webshop.de.

About Aldec

Aldec Inc., headquartered in Henderson, Nevada, is an industry leader in Electronic Design Verification and offers a patented technology suite including: RTL Design, RTL Simulators, Hardware-Assisted Verification, SoC and ASIC Prototyping, Design Rule Checking, IP Cores, Embedded, Requirements Lifecycle Management, DO-254 Functional Verification and Military/Aerospace solutions. www.aldec.com